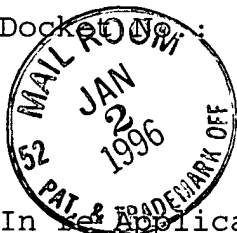


Docket No.:

394-159

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Seiji SAWADA et al.

Serial No. 08/246,582

Filed: May 19, 1994

For: TEST CIRCUIT IN CLOCK SYNCHRONOUS SEMICONDUCTOR MEMORY DEVICE

551/029
4/23/96

B.R.M.
#15
3-22-96

Group Art Unit: 2511

Examiner: A. Zarabian

Letter

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JAN 25 1996
GROUP 2500

365/201
elect.
Honorable Commissioner of
Patents and Trademarks
Washington, DC 20231

Sir:

On November 30, 1995, an amendment was inadvertently filed in the above-referenced application. Please disregard the amendment as it should have referenced the divisional application Serial Number 08/461,907, filed June 5, 1995. The amendment has be submitted in the correct application on January 2, 1996.

Respectfully submitted,

LOWE, PRICE, LEBLANC & BECKER

Edward J. Wise
Registration No. 34,523

99 Canal Center Plaza, Suite 300
Alexandria, Virginia 22314
(703) 684-1111 EJW:dls
Date: January 2, 1996